Vishay High Power Products

Schottky Rectifier, 2 x 6 A



- Popular D-PAK outline
- · Center tap configuration
- Small foot print, surface mountable
- · Low forward voltage drop
- · High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS directive 2002/95/EC
- · AEC-Q101 gualified

DESCRIPTION

The 12CWQ10FNPbF surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	12	A		
V _{RRM}		100	V		
I _{FSM}	t _p = 5 μs sine	330	A		
V _F	6 Apk, T _J = 125 °C (per leg)	0.65	V		
TJ	Range	- 55 to 150	°C		

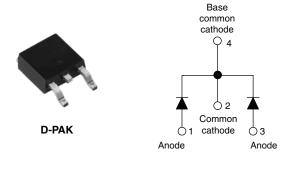
VOLTAGE RATINGS					
PARAMETER	SYMBOL	12CWQ10FNPbF	UNITS		
Maximum DC reverse voltage	V _R	100	V		
Maximum working peak reverse voltage	V _{RWM}	100			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average p	per leg	$I_{F(AV)}$ 50 % duty cycle at T _C = 135 °C, rectangular waveform		6	A
	device			12	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	330	A
non-repetitive surge current per leg See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	110	
Non-repetitive avalanche energy per	leg E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6	mJ
Repetitive avalanche current per leg		Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А

For technical questions, contact: diodestech@vishay.com







PRODUCT SUMMARY				
I _{F(AV)}	2 x 6 A			
V _R	100 V			

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
	V _{FM} ⁽¹⁾	6 A	T _J = 25 °C	0.80	V
Maximum forward		12 A		0.95	
voltage drop per leg See fig. 1		6 A	T 405 00	0.65	
		12 A	T _J = 125 °C	0.78	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1	mA
See fig. 2		T _J = 125 °C	VR - Haleu VR	4	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.47	V
Forward slope resistance	r _t			20.68	mΩ
Typical junction capacitance per leg	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$, (test signal range 100 kHz to 1 MHz), 25 °C		183	pF
Typical series inductance per leg	LS	Measured lead to lead 5 m	5.0	nH	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T_{J} ⁽¹⁾ , T_{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case	per leg	- R _{th.IC}	DC operation	3.0	°C/W
	per device		See fig. 4	1.5	
Approximate weight				0.3	g
				0.01	oz.
Marking device			Case style D-PAK (similar to TO-252AA)	12CW0	Q10FN

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

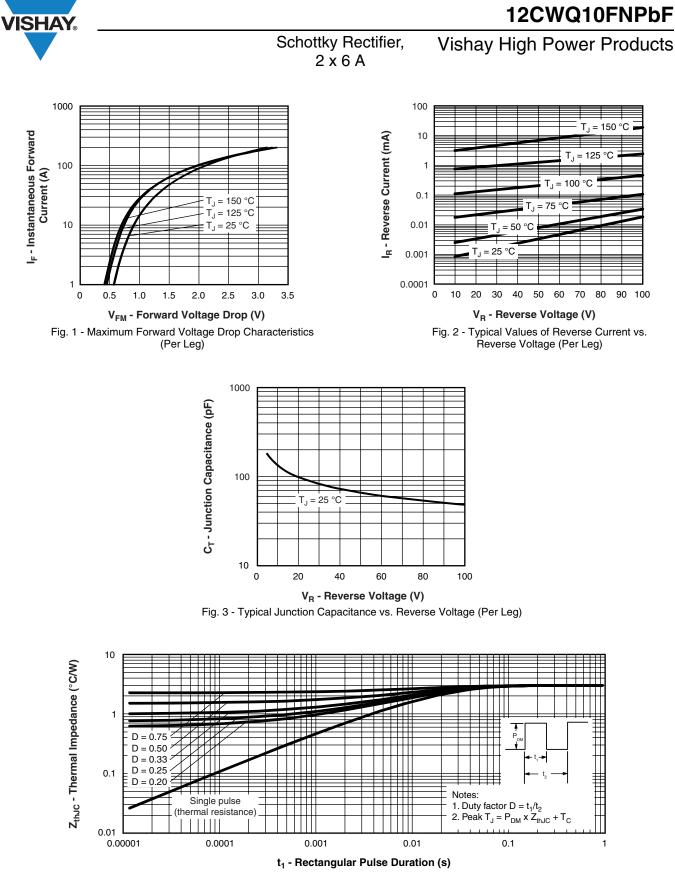


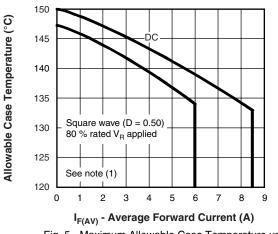
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

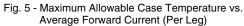
12CWQ10FNPbF

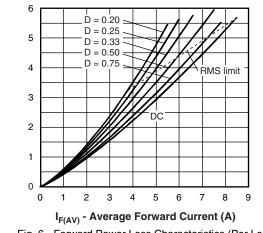
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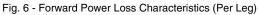
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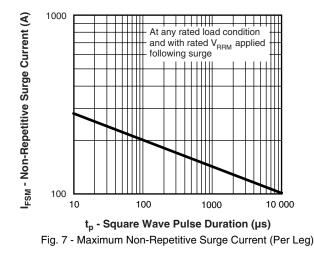
Average Power Loss (W)











Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

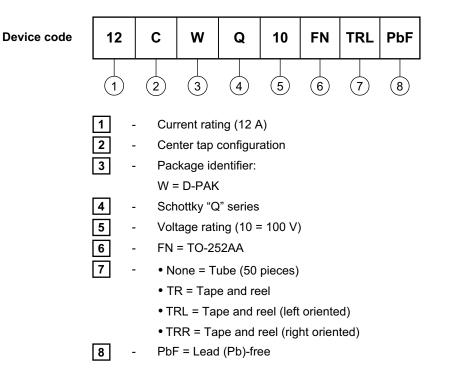
 $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 - D); I}_{R} \mbox{ at } \mbox{V}_{R1} = 80 \ \% \mbox{ rated } \mbox{V}_{R} \end{array}$



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95016			
Part marking information	www.vishay.com/doc?95059			
Packaging information	www.vishay.com/doc?95033			
SPICE model	www.vishay.com/doc?95177			



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